

EAST Search History

Ref. #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"6985711".pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 06:59
L2	1	1 and (resistor same capacitor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:08
L3	43004	(DC or (direct adj2 current)) near5 (eliminat\$3 or correct\$3 or compensat\$3 or reduc\$3 or remov\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:09
L4	710	3 same (capacitor same switch\$3 same resistor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:30
L5	99926	"455"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:10
L6	39	4 and 5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:10
L7	5	3 same (capacitor same (switch\$3 adj (network or system)) same resistor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:31
L8	76	4 and (switch\$3 adj2 (network or system))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:33
L9	1	8 and (plurality adj2 capacitor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:32
L10	6	5 and 8	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:38
L11	99	4 and (second adj2 capacitor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 08:03
L12	13	4 and ((chang\$3 or adjust\$3) with bandwidth with (resistor or capacitor))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:56
L13	509	((chang\$3 or adjust\$3) with bandwidth with (resistor or capacitor))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:56
L14	54	3 and 13	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:56
L15	5	5 and 14	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 07:56

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L16	4	3 and ((second adj2 capacitor) same ((chang\$3 or adjust\$3) with (bandwidth or band)) same (first adj3 capacitor))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 08:56
L17	9	("6985711" OR "20040038649" OR "6756924" OR "20040071238" OR "20050239428" OR "20040012439" OR "6560447" OR "20040146122" OR "6356217").PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 08:58
L18	6	17 and (bandwidth same filter)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 09:47
L19	1	17 and (serial adj2 bus)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 09:52
L20	1	17 and ((microprocessor or processor) with stor\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:00
L21	5	17 and mode	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:06
L22	0	17 and sleep	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:07
L23	0	17 and idle	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:07
L24	1	17 and standby	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:11
L25	1	3 and ((increas\$3 adj bandwidth) with sleep)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:12
L26	1	(offset or DC) and ((increas\$3 adj bandwidth) with sleep)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:15
L27	77	3 and (increas\$3 with sleep)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:14
L28	2	5 and 27	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:14
L29	72	3 and ((increas\$3 adj bandwidth) with compress\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:20
L30	0	5 and 29	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:16

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L31	63760	"375"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:17
L32	0	29 and 21	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:17
L33	2	29 and 31	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:18
L34	39985	"330"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:19
L35	1	29 and 34	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/07/31 10:19